

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
: Examiner: M. Santiago  
TETSUYA KANEKO, ET AL. )  
: Group Art Unit: 2879  
Application No.: 09/960,744 )  
: Confirmation No.: 5038  
Filed: September 24, 2001 )  
: September 27, 2004  
For: METHOD OF MANUFACTURING )  
: IMAGE DISPLAY APPARATUS )



**Mail Stop Issue Fee**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

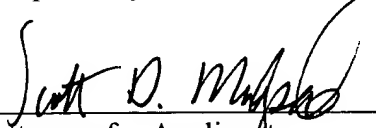
Sir:

In response to the Examiner's Statement of Reasons for Allowance, attached to the Notice of Allowability mailed August 31, 2004, Applicants respectfully wish to file the following remarks.

Initially, with respect to Claims 10, 18 and 26, these claims recite the panel getter step is performed "between" another step and the seal-bonding step. In addition, Claim 20 recites that the voltage application step applies a voltage to the electron emitting device "subjected to the activation process," but Claims 21, 28 and 29 do not include such a feature.

Applicants' undersigned attorney may be reached in our Washington, D.C. office by telephone at (202) 530-1010. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,

  
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